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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: Theodore W. Houston

Docket No.: TI-35657

Serial No.: 10/667,615

Art Unit: 2811

Filed: 9/22/2003

Examiner: Owens, Douglas

Title: A Multi-Gate One-Transistor Dynamic Random Access Memory

AMENDMENT UNDER 37 CFR §1.111

February 4, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Commissioner:

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)

I hereby certify that the above correspondence is being deposited
with the U.S. Postal Service on 2-4-05
as First Class Mail in an envelope addressed to: Commissioner for
Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Karen Vertz
Karen Vertz

2-4-05
Date

In response to the Office Action, dated 10/06/2004, in the above-identified patent application, please make the following amendments. They are respectfully submitted as a full and complete response to that Action. Please charge any required fees to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.